

DLD Course Assignment#6

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Section:   
CS-D

**Q-1**

**State Diagram:**



**Q-2  
(a)  
  
  
  
  
  
  
(b)  
  
  
  
  
(c)  
  
  
  
  
Q-3  
  
**

**Q-4  
**

**Q-5  
  
**

**Q-6  
**

**Q-7  
**

**Q-8  
  
Q-9  
Circuit Diagram:  
16 Bit R:**

**16 Bit Comparator:  
Q-10  
  
**

**Q-11**

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**THANK YOU**

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